

FIG. 1
PRIORITY ART

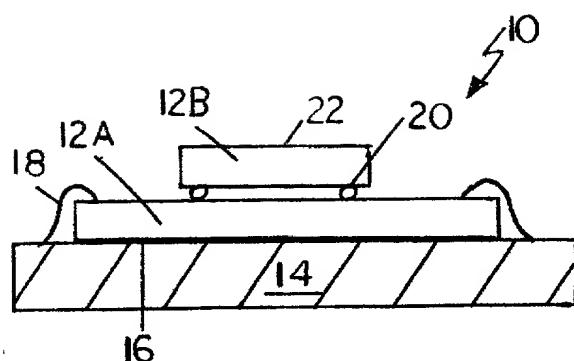


FIG. 3
PRIORITY ART

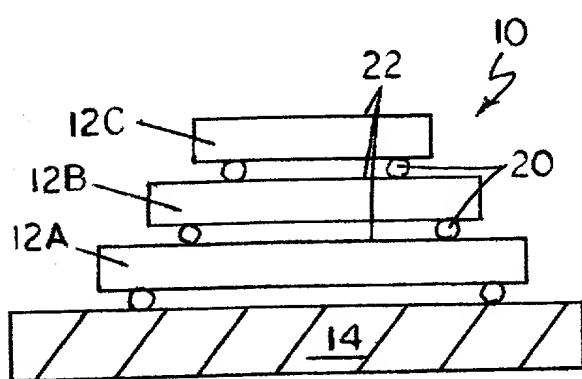


FIG. 2
PRIORITY ART

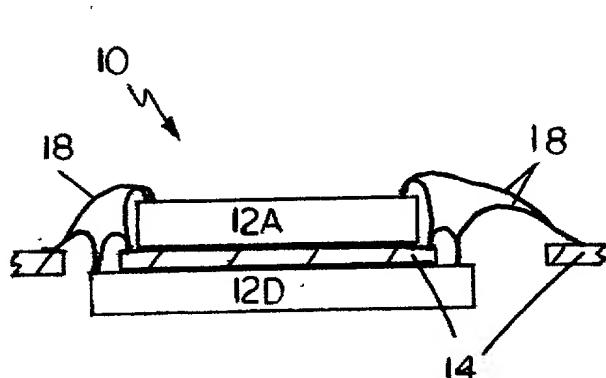


FIG. 4
PRIORITY ART

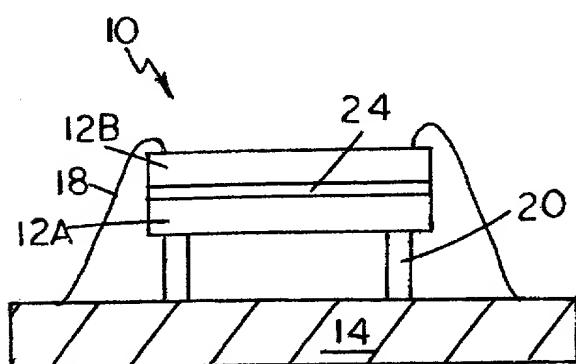


FIG. 5
PRIORITY ART

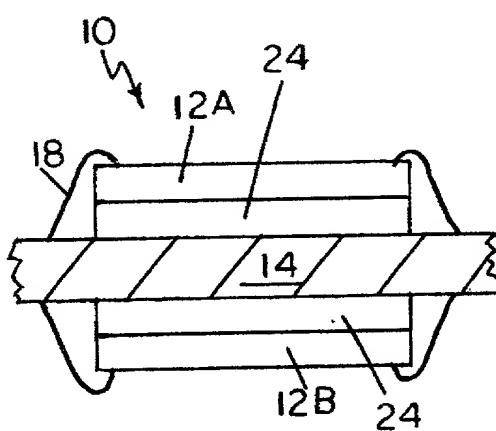


FIG. 6
PRIORITY ART

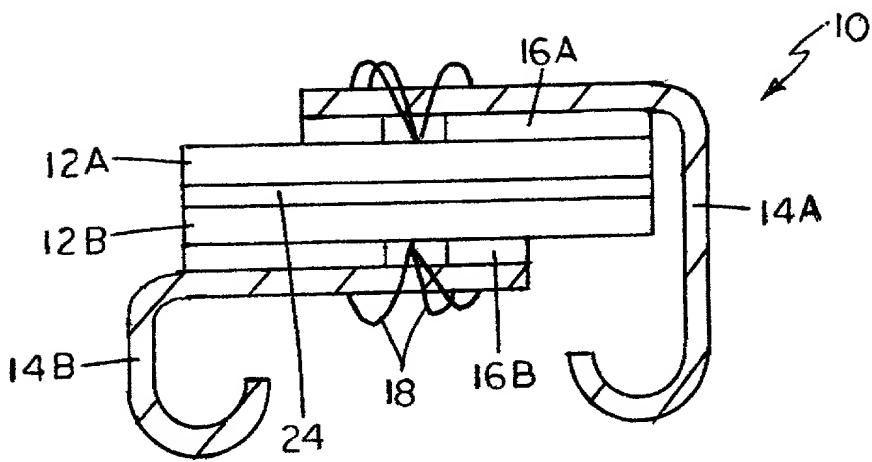


FIG. 7
PRIOR ART

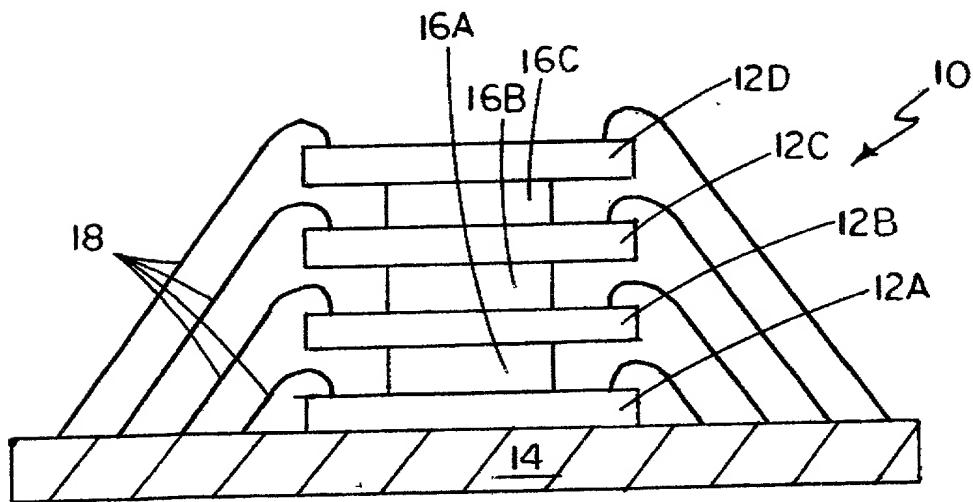


FIG. 8
PRIOR ART

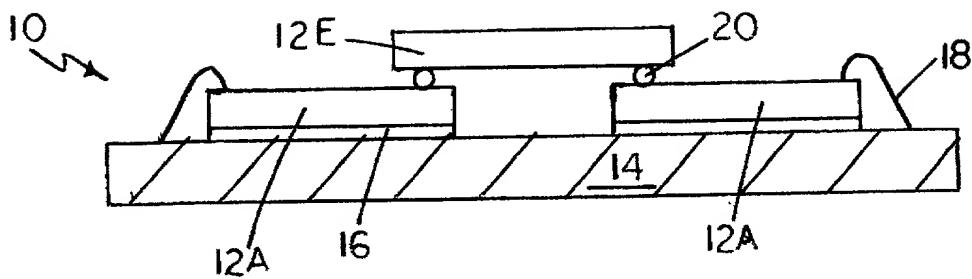


FIG. 9
PRIOR ART

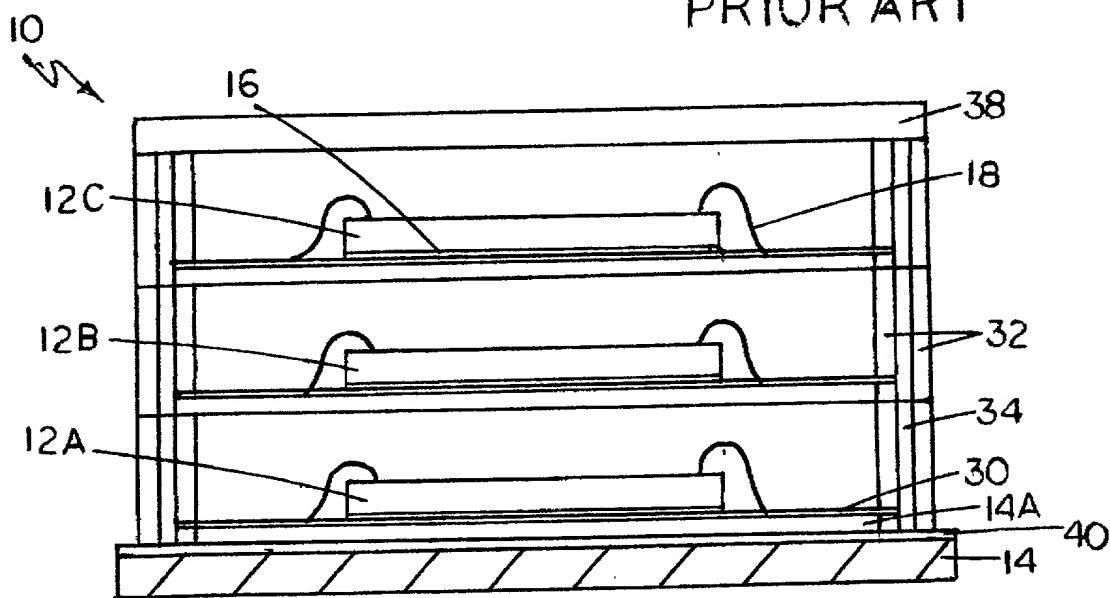


FIG. 10
PRIOR ART

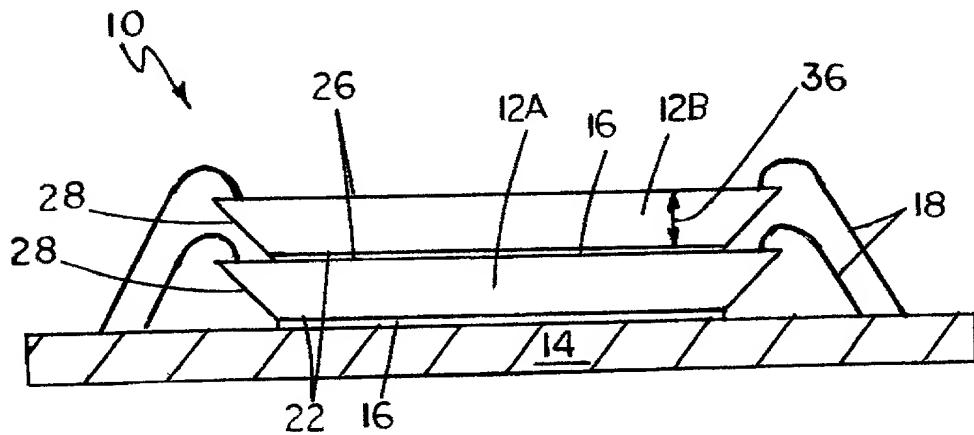


FIG. 11
PRIOR ART

FIG. 12

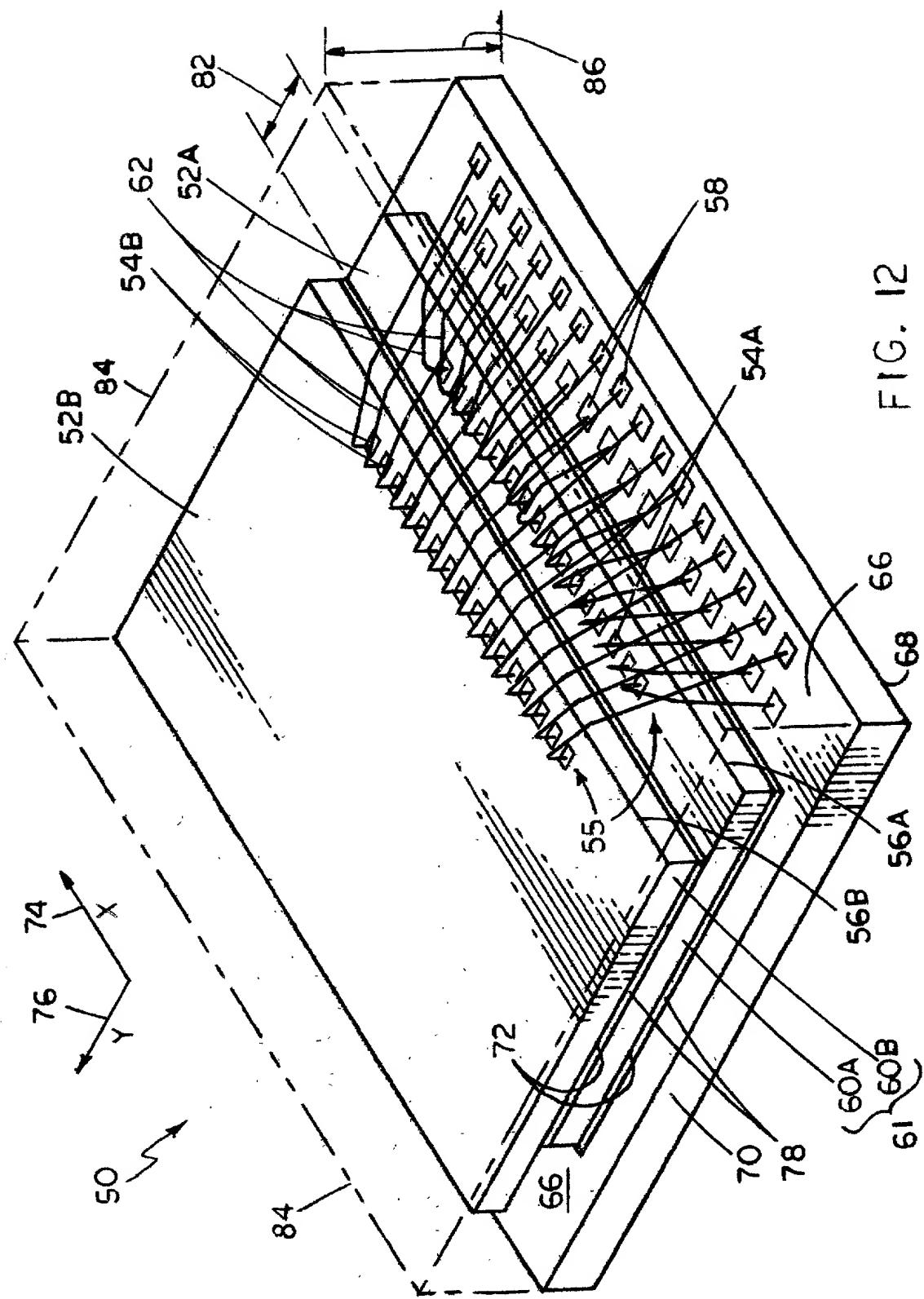


FIG. 12A

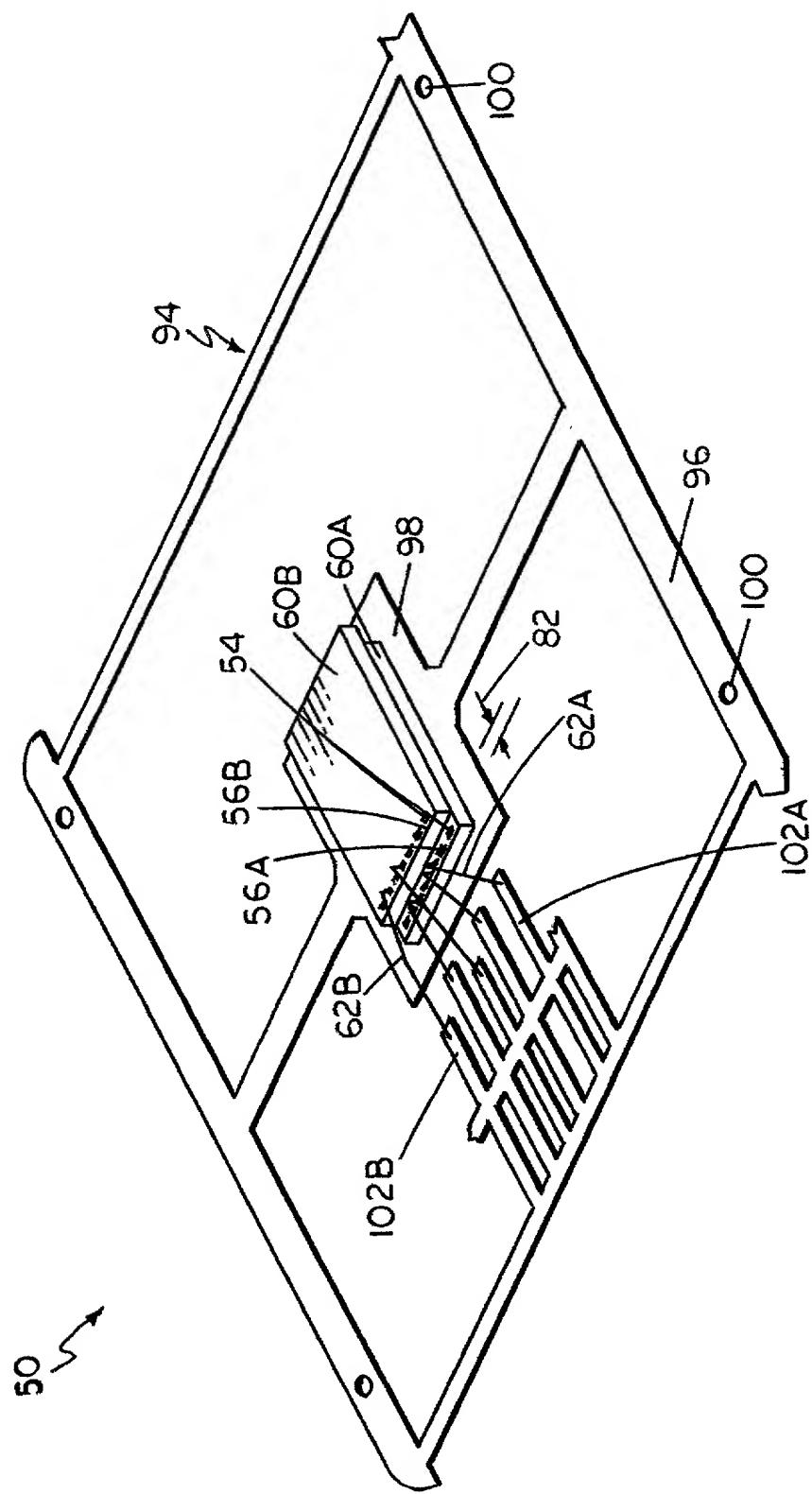


FIG. 12B

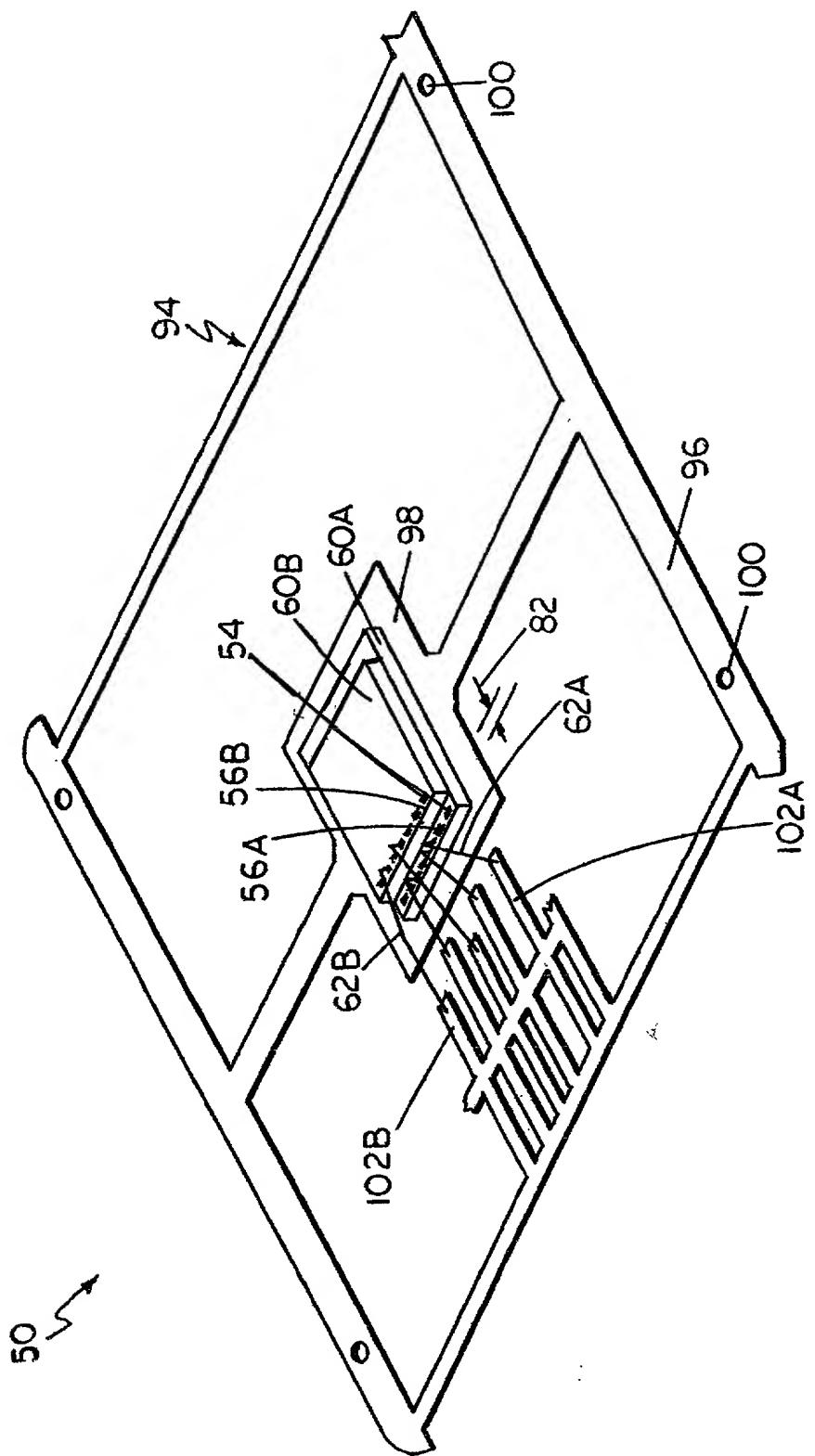
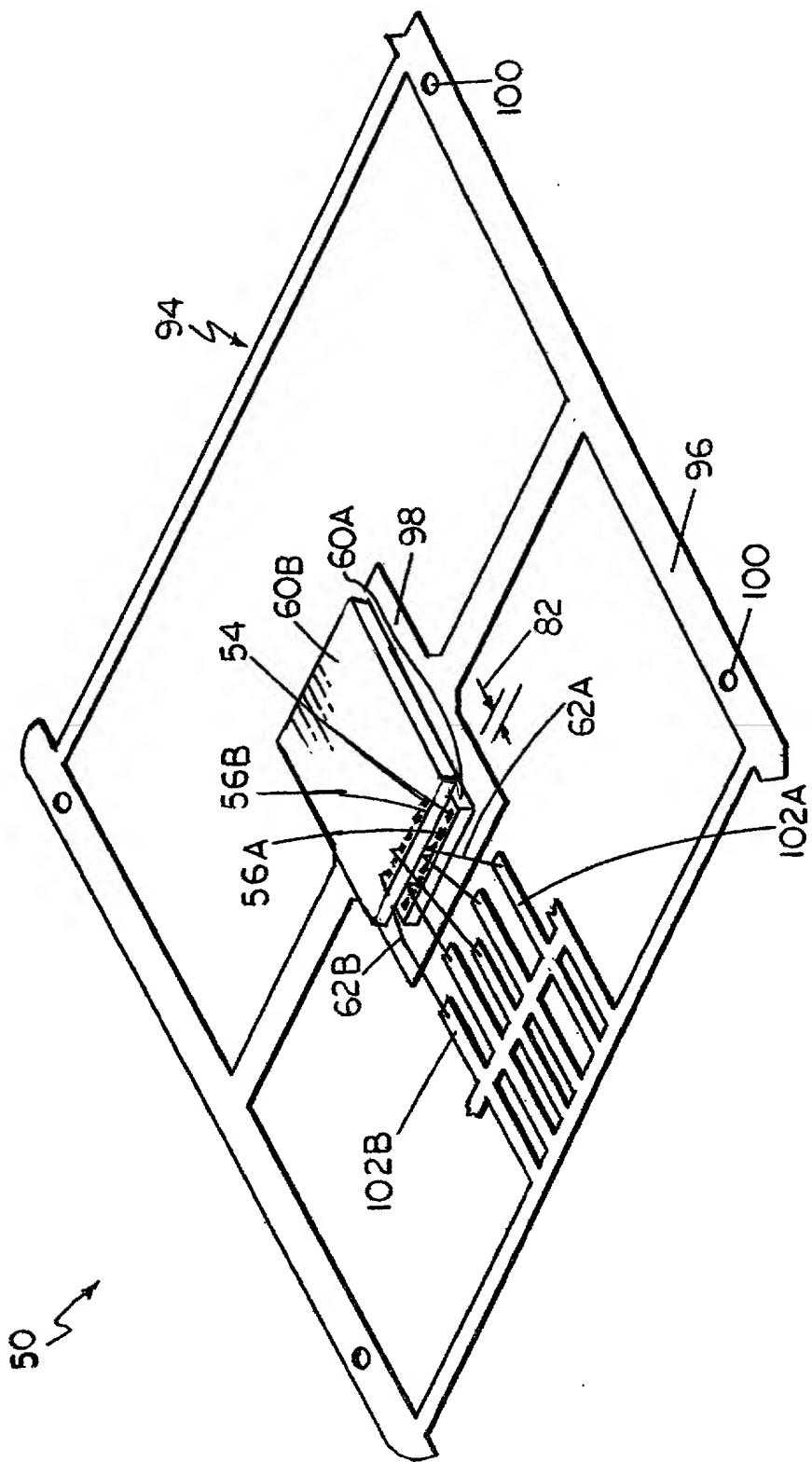


FIG. I2C



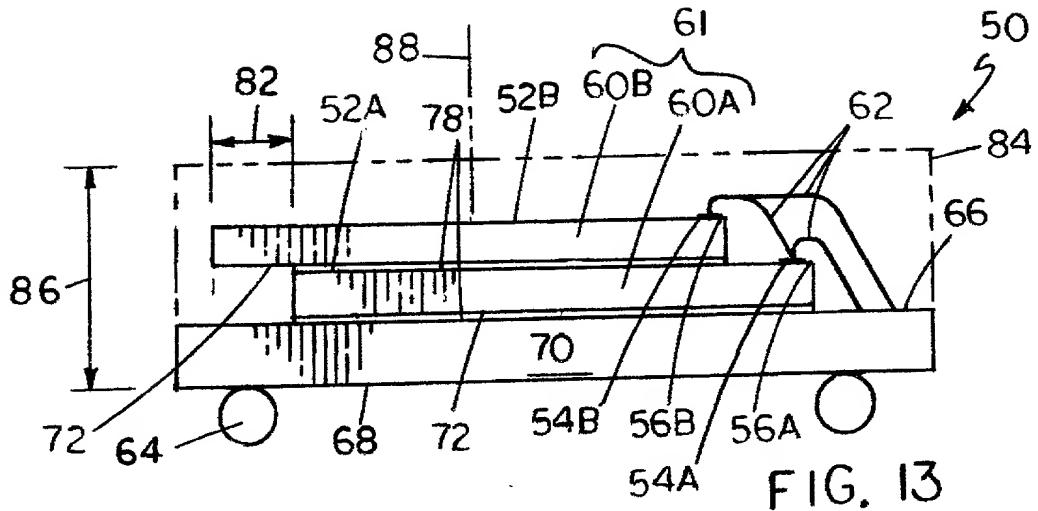


FIG. 13

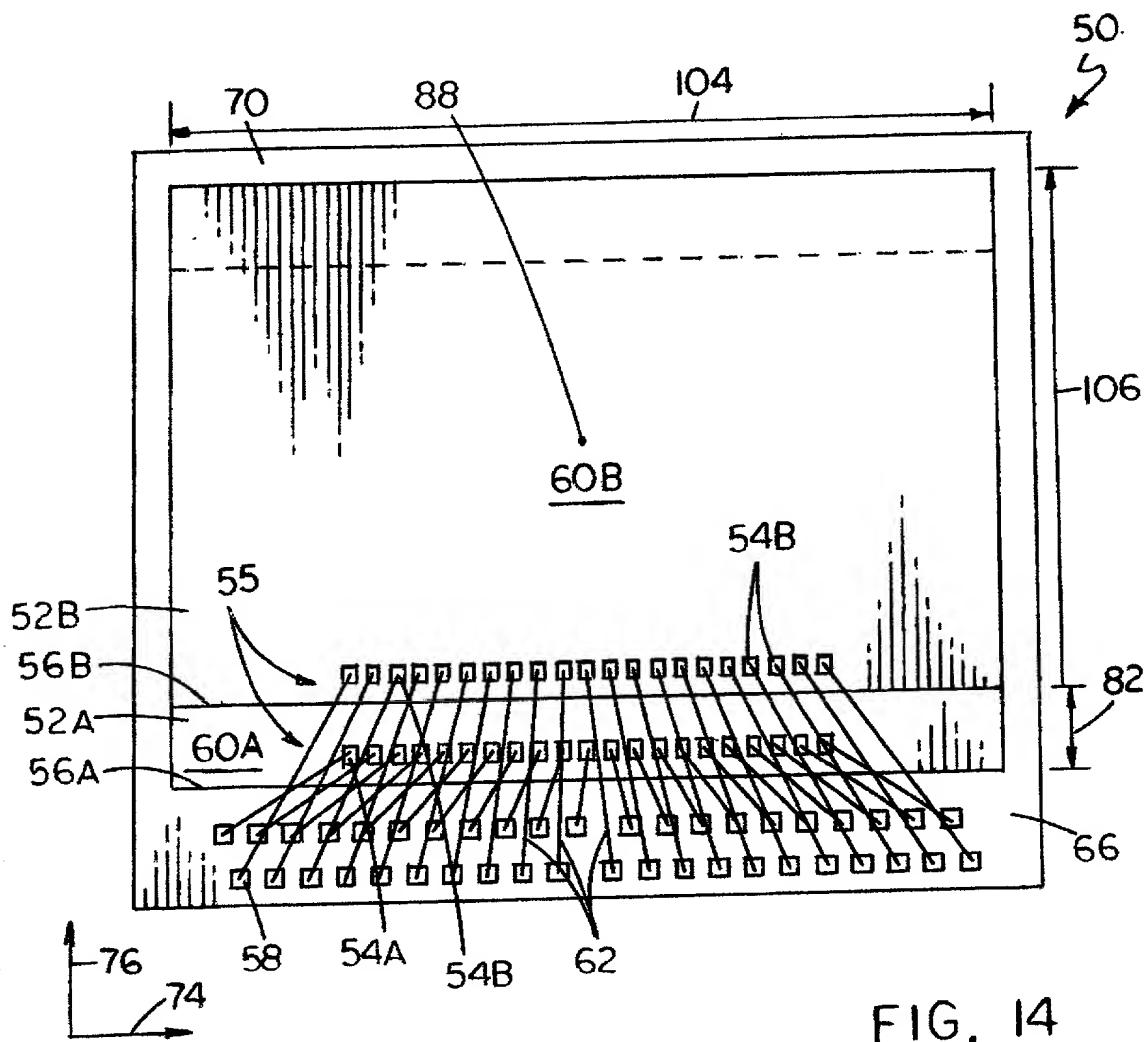


FIG. 14

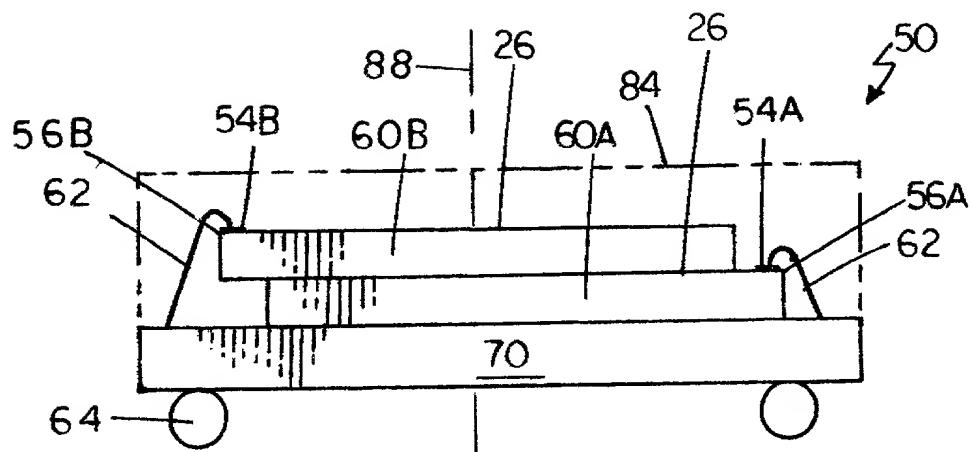


FIG. 15

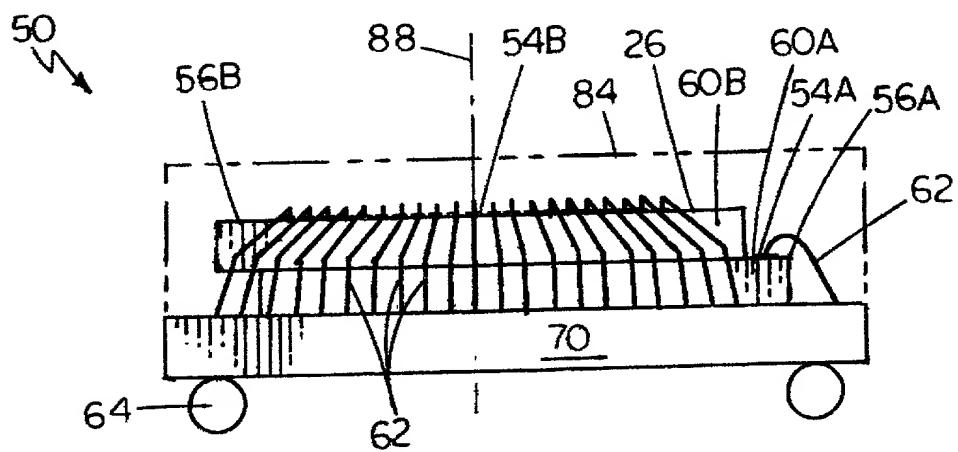


FIG. 16

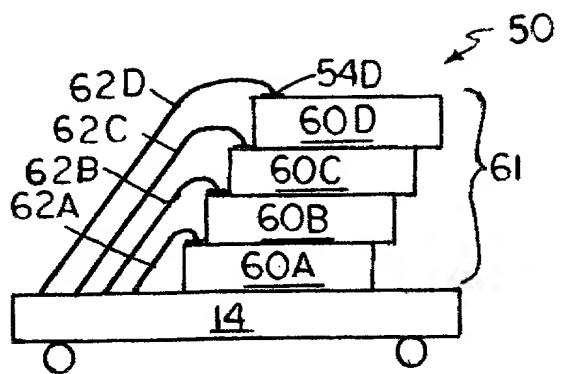


FIG. 17

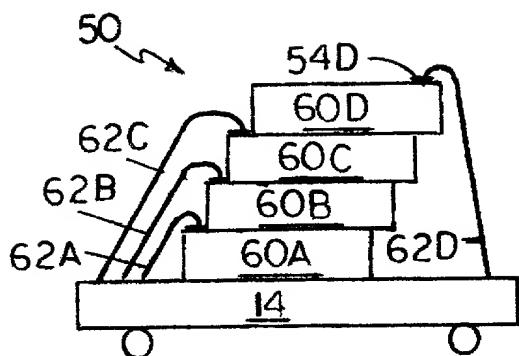


FIG. 18

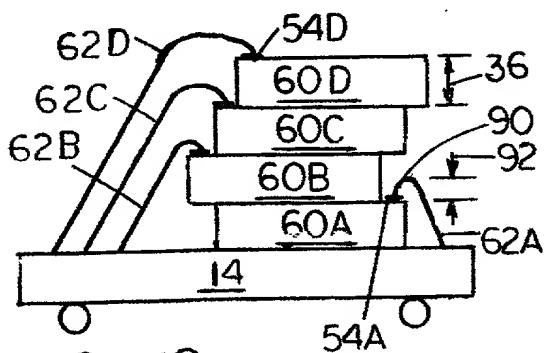


FIG. 19

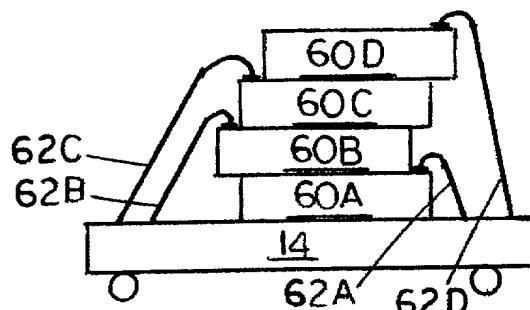


FIG. 20

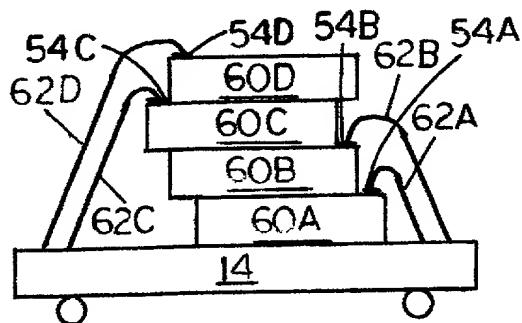


FIG. 21

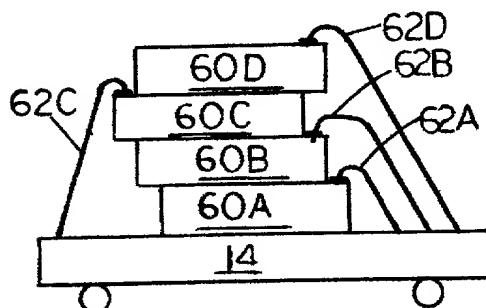


FIG. 22

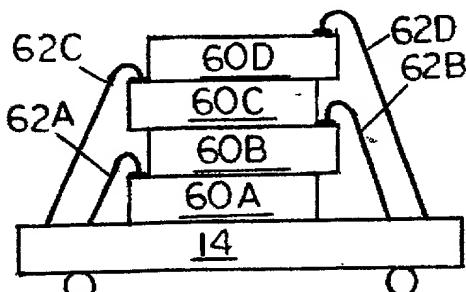


FIG. 23

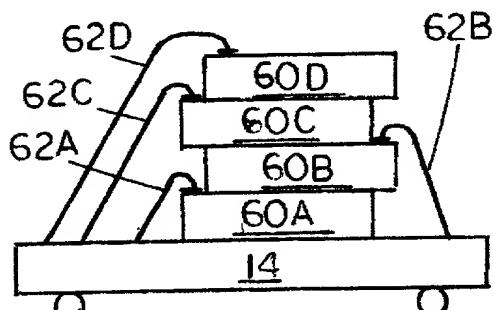


FIG. 24

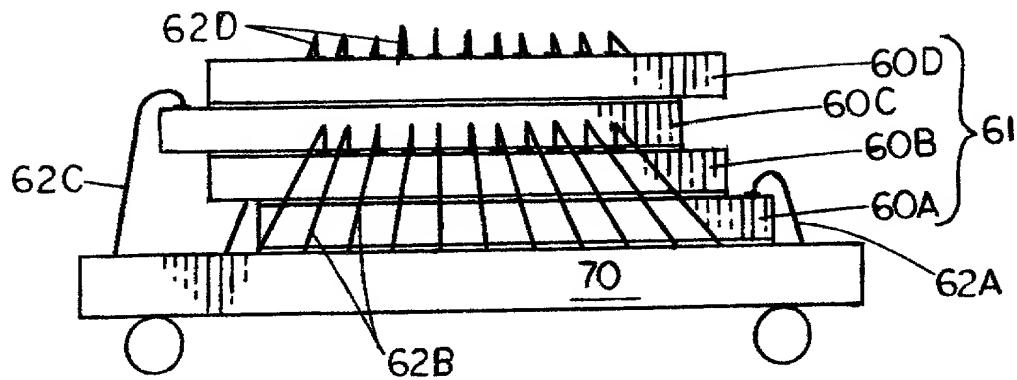


FIG. 25

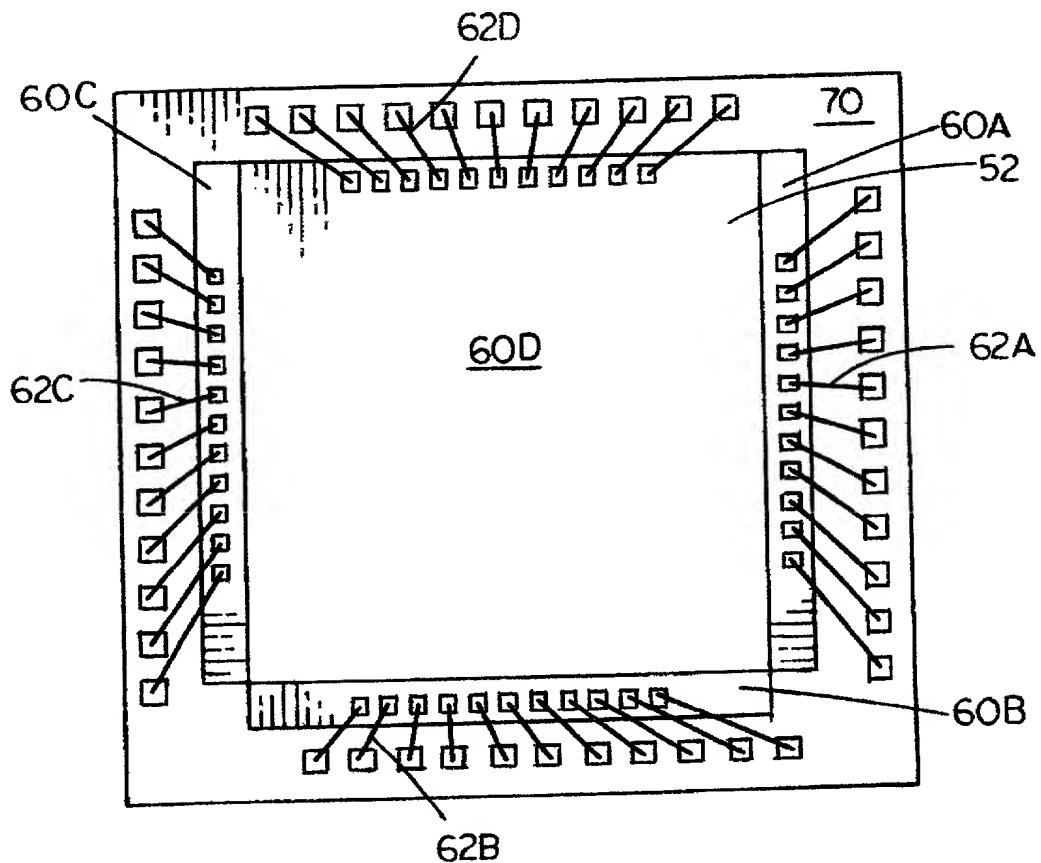
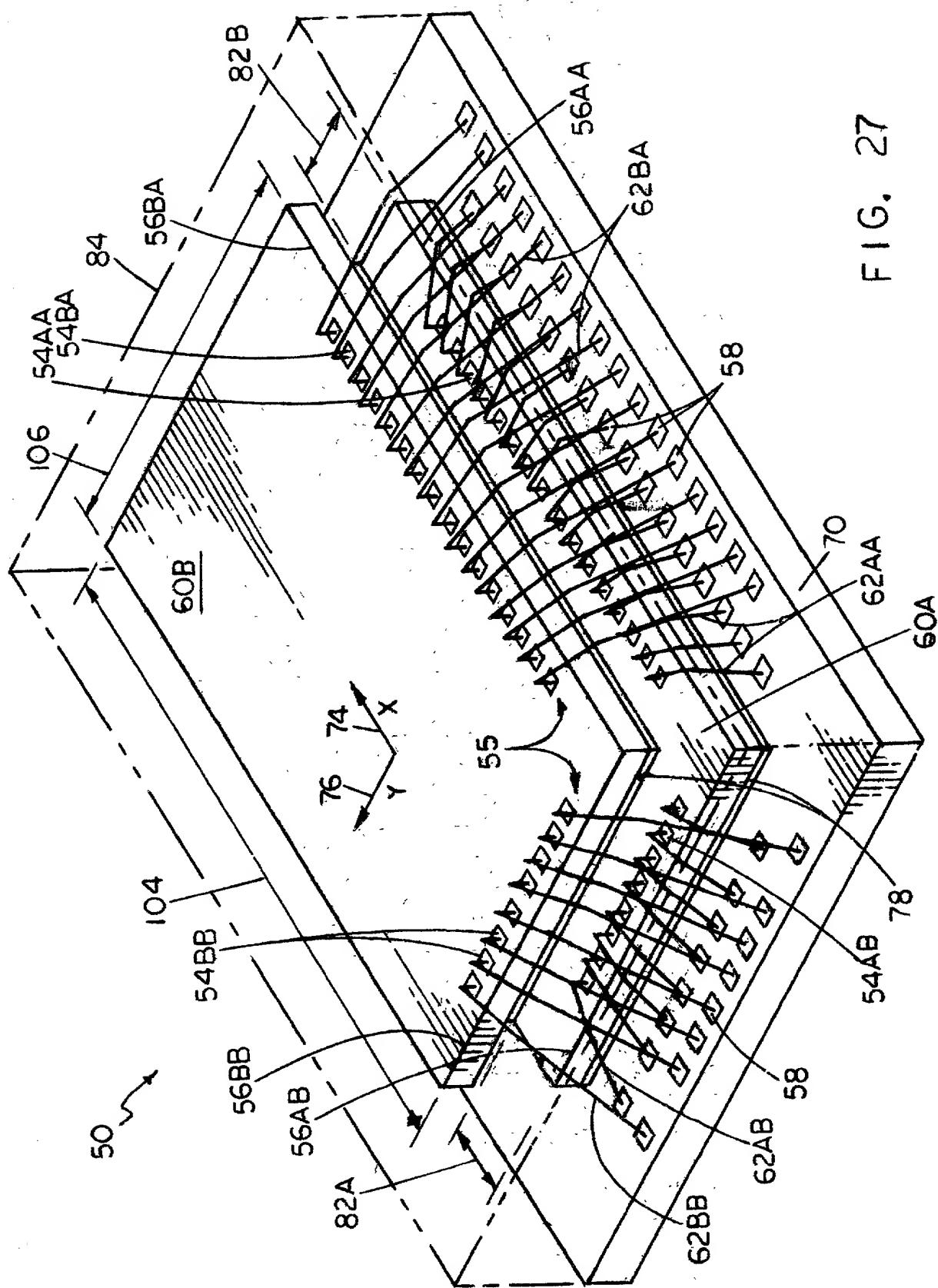


FIG. 26

FIG. 27



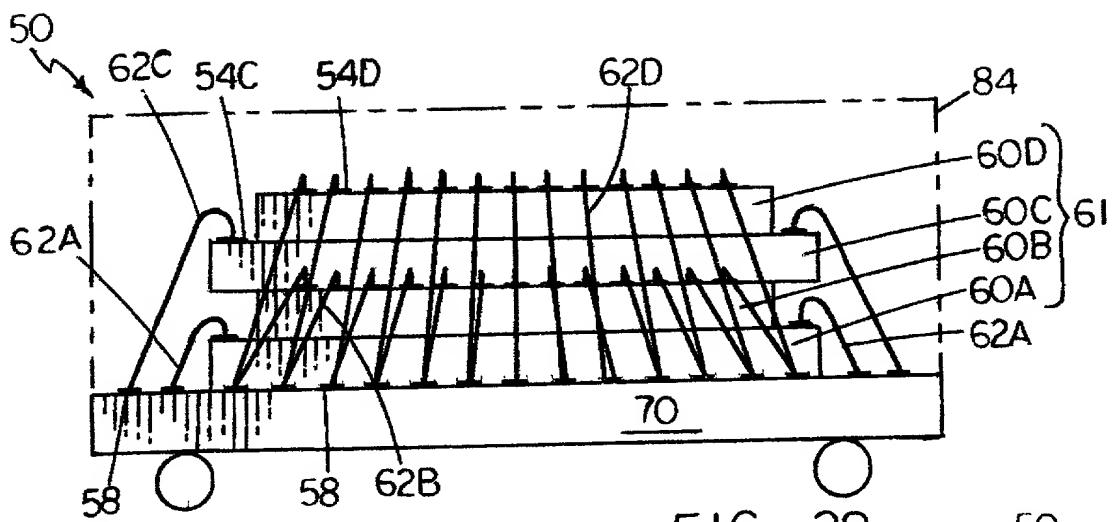


FIG. 28

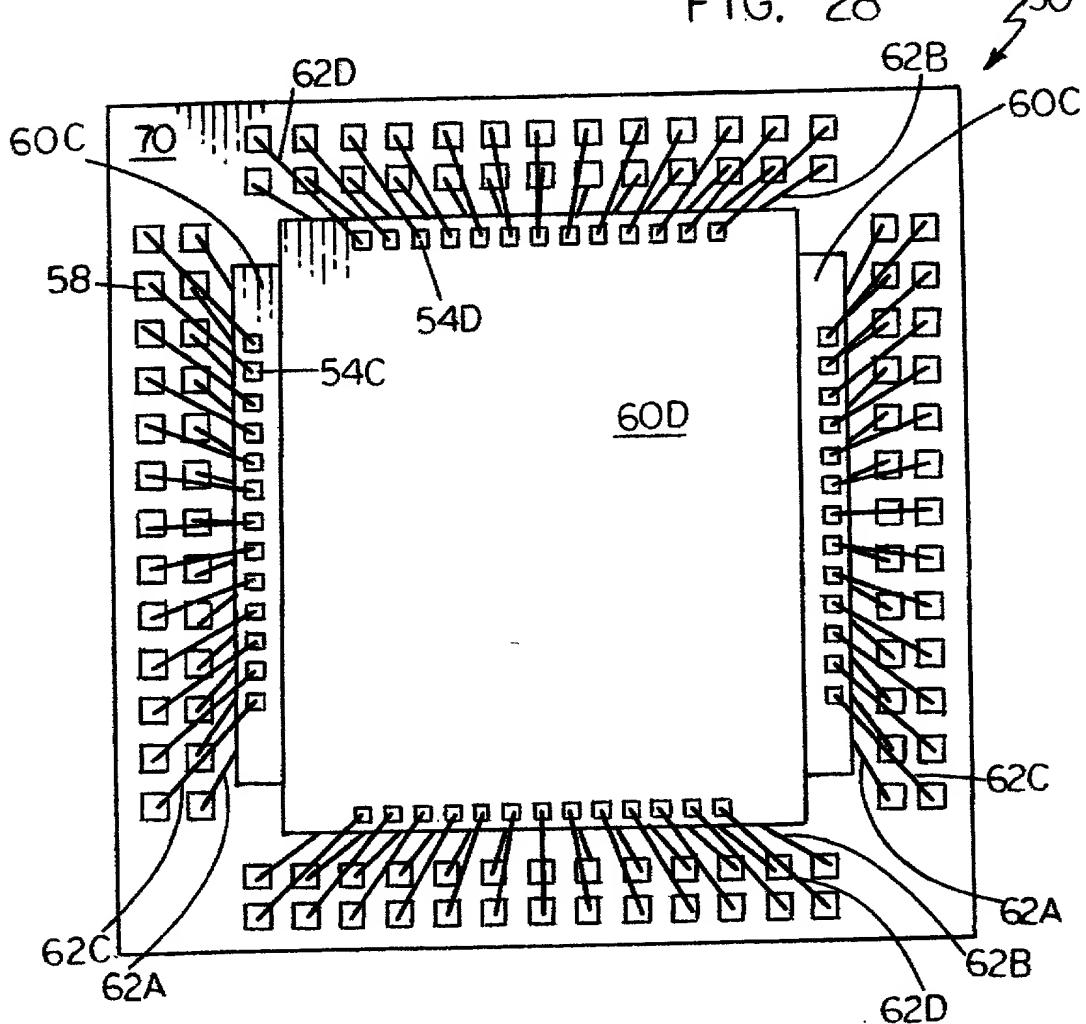


FIG. 29